Notice of Allowability	Application No.	Applicant(s)	
	09/409,940	BULLIS ET AL.	
	Examiner	Art Unit	
	Fred Ferris	2128	
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R	(OR REMAINS) CLOSED or other appropriate comr IGHTS. This application is	in this application. If not include nunication will be mailed in due	ied course. THIS
1. This communication is responsive to <u>5 December 2005</u> .			
2. The allowed claim(s) is/are <u>1-23</u> .			
 Acknowledgment is made of a claim for foreign priority una)	be been received. been received in Applicate cuments have been received of this communication to fi	tion No ed in this national stage applica	
 4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give 5. CORRECTED DRAWINGS (as "replacement sheets") must 	es reason(s) why the oath	XAMINER'S AMENDMENT or I or declaration is deficient.	NOTICE OF
(a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1	son's Patent Drawing Revie s Amendment / Comment	or in the Office action of	o hook) of
each sheet. Replacement sheet(s) should be labeled as such in t	he header according to 37 (CFR 1.121(d).	e back) of
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			Note the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5 ☐ Notice of I	Informal Patent Application (PT	(O-152)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview	Summary (PTO-413),	
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No '8). 7. ☐ Examiner	o./Mail Date s Amendment/Comment	
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	_	s Statement of Reasons for All	owance
		PRIMARY EXAM	ince

Application/Control Number: 09/409,940

Art Unit: 2128

DETAILED ACTION

1. This Office Action is responsive to applicant's amendment filed 5 December 2005. Claims 1-23 are currently pending in this application. Amended claims 1-23 have now been allowed over the prior art of record.

Response to Arguments

2. Applicant's arguments filed 5 December 2005 have been fully considered and found to be persuasive.

Regarding applicant's response to 103(a) rejection: The examiner withdraws the 103(a) rejection in view of applicant's amendment to the claims and arguments presented 5 December 2005.

Allowable Subject Matter

3. Claims 1-23 have now been allowed over the prior art of record.

The following is an examiner's statement of reasons for allowance:

Applicants are disclosing a method and system for providing simulation of an integrated circuit that is under development and includes an island with a coupled interface including a snooper, a checker and a generator for obtaining an output and providing a test case. This has been disclosed in the prior art of record.

While these elements are individually disclosed in the prior art, the prior art of record does not meet the conditions as suggested in MPEP section 2132, namely:

Application/Control Number: 09/409,940

Art Unit: 2128

"The identical invention must be shown in as complete detail as is contained in the ... claim." Richardson v. Suzuki Motor Co., 868 F.2d 1226, 1236, 9 USPQ2d 1913, 1920 (Fed. Cir. 1989). The elements must be arranged as required by the claim, but this is not an **ipsissimis verbis** test, i.e., identity of terminology is not required. **In re Bond**, 910 F.2d 831, 15 USPQ2d 1566 (Fed. Cir. 1990)."

In particular, the examiner notes that while features such as the use of a snooper, a checker and a generator for obtaining an output and providing a test case to an integrated circuit under development are well known, and disclosed in the prior art, these features are not explicitly disclosed or rendered obvious as being <u>in combination</u> with a test case coupled to the generator providing data, and a request that the generator perform a particular simulation on the island, without the test case including locic for directly controlling the functions of the snooper, checker, and generator as now required by amended independent claims 1, 10, and 17. (See specification: page 14, line 5 to page 17, line 23, Figs. 3-6, for example)

The closest prior art uncovered during examination teaches certain limitations of the claimed invention as follows:

<u>U.S. Patent 6,182,258 issued to Hollander</u>: Teaches elements relating to verifying an integrated circuit design that monitors (i.e. snoops) the simulation of an integrated circuit design via a checker (with an interface) and includs a test generator using a test case with automation (i.e. intelligence for directing test) for determining the defective behavior (for desired output) of the circuit. Hollander does not however explicitly disclose these features in combination with a test

Application/Control Number: 09/409,940

Art Unit: 2128

case coupled to the generator providing data, and a request that the generator perform a particular simulation on the island, without the test case including logic for directly controlling the functions of the snooper, checker, and generator as now required by amended independent claims 1, 10, and 17 of the present invention.

<u>U.S. Patent 6,006,024 issued to Guruswamy</u>: Teaches a cell layout generation system environment that includes islands for an integrated circuit design, but again, does not explicitly disclose the required features in combination with a test case coupled to the generator providing data, and a request that the generator perform a particular simulation on the island, without the test case including logic for directly controlling the functions of the snooper, checker, and generator as now required by amended independent claims 1, 10, and 17 of the present invention.

The features noted above relating to the specific combination of elements, renders the claimed invention non-obvious over the prior art of record. Dependent claims 2-9, 11-16, and 18-23 are deemed allowable as depending from independent claims 1, 10, and 17 respectively.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Art Unit: 2128

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fred Ferris whose telephone number is 571-272-3778 and whose normal working hours are 8:30am to 5:00pm Monday to Friday. Any inquiry of a general nature relating to the status of this application should be directed to the group receptionist whose telephone number is 571-272-3700. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jean Homere can be reached at 571-272-3780. The Official Fax Number is: 571-273-8300

Fred Ferris, Primary Examiner
Simulation and Emulation, Art Unit 2128
U.S. Patent and Trademark Office
Randolph Building, Room 5D19
401 Dulany Street
Alexandria, VA 22313
Phone: (571-272-3778)
Fred.Ferris@uspto.gov

February 21, 2006

PRIMANI EXAMINESS